



For Immediate Release

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**CIRANOVA APPOINTS ERIC FILSETH AS CEO;
EDA veteran completes management team**

SANTA CLARA, Calif., September 19, 2007 – Ciranova, Inc, an EDA start-up developing open and robust automated layout technology for analog and custom integrated circuit design, announced that Eric Filseth has been appointed chief executive officer, effective today.

"We are very pleased to have Eric Filseth leading Ciranova as CEO," said Dan Rubin, chairman of Ciranova's board of directors. "Eric brings to Ciranova his proven leadership ability, long EDA industry expertise, extensive product management experience, and entrepreneurial energy. Eric has already made significant contributions in the EDA industry, and we are confident that he is the best person to lead the future development of Ciranova's business."

Filseth spent a total of seventeen years with Cadence Design Systems, where he was most recently marketing vice president for Digital Implementation products. He also held executive positions with Silicon Perspective Corporation and Sente, Inc. Previously he managed product marketing for analog, RF and mixed-signal products at Cadence. He started his career in 1983 as an analog design engineer with National Semiconductor. Filseth received an MBA from UCLA and a BSEE from Stanford University.

"Analog accounts for a third of the non-memory semiconductor market, yet automation for analog design lags far behind that for digital design," said Filseth. "Ciranova is ideally positioned to help bridge that gap, and bring tremendous productivity increases to analog and custom designers. I'm impressed by what the company has accomplished to date, and I'm excited to be joining the Ciranova team."

"Our search for a CEO to lead Ciranova to the next stage of growth was focused and thorough," said Ed Petrus, Ciranova COO and vice president of engineering. "Eric became our first choice very early in the process. He will make an ideal addition to the Ciranova team."

About Ciranova

Ciranova™ is an Electronic Design Automation (EDA) startup company, focused on building open, interoperable technology for analog and mixed signal (AMS) physical design automation. Ciranova's technology, built from the ground up on the industry standard OpenAccess database from Si2, will bring the integration, productivity and migration benefits of modern automation tools to custom and AMS designers. The company's first product, PyCell Studio™, is available as a free download from its web site, www.ciranova.com. The company is headquartered in Santa Clara, California.

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Note to editors: Digital photo available upon request.