



**For Immediate Release**

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CIRANOVA, INC. SECURES \$5.25 MILLION IN FUNDING;  
Synopsys and Mentor Graphics Participate with Previous Investors Alloy Ventures, USVP,  
Asiatech Management and Cadence Founder Jim Solomon

SANTA CLARA, Calif., March 24, 2008 – Ciranova, Inc., an EDA start-up developing OpenAccess-based interoperable layout technology for analog and custom integrated circuit design, today announced that it has closed a round of financing totaling \$5.25 million. Industry leaders Synopsys, Inc. and Mentor Graphics Corporation both participated in the round, along with previous investors Alloy Ventures (lead investor), US Venture Partners, Asiatech Management and Cadence founder Jim Solomon. As part of the funding agreement, both Mentor and Synopsys will obtain Board Observer positions, although the makeup of the Ciranova Board of Directors will not change.

“Ciranova’s interoperable PyCells do more than just opening the playing field,” said Paul Lo, Senior Vice President and General Manager, Analog and Mixed Signal Group at Synopsys. “The Ciranova technology is fundamentally better suited to supporting advanced layout automation. New analog/mixed-signal design systems that take full advantage of PyCells will enable higher levels of productivity than flows tied to legacy PCell implementations.”

“There is enormous pent-up user demand for better PCell technology, especially at advanced geometries,” said Jue-Hsien Chern, Vice President and General Manager, Deep Submicron Division at Mentor Graphics. “Ciranova’s architecture enables interoperability and additional functionality. Building on OpenAccess makes the technology accessible from within a broad array of EDA tools, from physical layout to physical verification.”

“This round of funding was strategic for Ciranova,” said Eric Filseth, Ciranova CEO. “We have been working very closely with Synopsys and Mentor Graphics to ensure interoperability of their tools with Ciranova Interoperable PCell Libraries. We share their commitment to open and inclusive physical design, and we’re excited to have them as investors.”

The capital will be used to further develop, deploy and support Ciranova’s innovative suite of interoperable analog and custom physical design technology.

### **About Synopsys**

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in software and IP for semiconductor design and manufacturing. The company delivers technology-leading system and semiconductor design and verification platforms, IC manufacturing and yield optimization solutions, semiconductor intellectual property and design services to the global electronics market. These solutions enable the development and production of complex integrated circuits and electronic systems. Through its comprehensive solutions, Synopsys addresses the key challenges designers and manufacturers face today, including power management, accelerated time to yield and system-to-silicon verification. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at [www.synopsys.com](http://www.synopsys.com).

### **About Mentor Graphics**

Mentor Graphics Corporation (Nasdaq: MENT) is a world leader in electronic hardware and software design solutions, providing products, consulting services and award-winning support for the world’s most successful electronics and semiconductor companies. Established in 1981, the company reported revenues over the last 12 months of over \$875 million and employs approximately 4,350 people worldwide. Corporate headquarters are located at 8005 S.W. Boeckman Road, Wilsonville, Oregon 97070-7777. World Wide Web site: [www.mentor.com](http://www.mentor.com).

### **About Ciranova**

Ciranova™ is an Electronic Design Automation (EDA) startup company, focused on building an open, interoperable, language-based cell generator technology for custom analog and mixed-signal (AMS) integrated circuit design. Ciranova’s technology, which is the only layout generator development environment built from the ground up on the industry standard OpenAccess database from SI2, will bring the integration, productivity and migration benefits of modern language-based design to custom and AMS designers. The company’s first product, PyCell Studio™, is available as a free download from its web site, [www.ciranova.com](http://www.ciranova.com). The company is headquartered in Santa Clara, California.

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