



For Immediate Release

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EDA INNOVATOR CIRANOVA STRENGTHENS MANAGEMENT TEAM
WITH ADDITION OF INDUSTRY VETERAN MICHAEL MA
Ma joins as VP Business Development,
Company expands role for SKILL Pioneer Petrus to COO

SANTA CLARA, Calif., October 24, 2006 – Ciranova, Inc, an EDA start-up developing open and robust layout generation technology for analog and custom integrated circuit design, today announced that EDA industry veteran Michael Ma has joined the company as vice president of business development. Ciranova also announced that Ed Petrus, one of the original developers of the SKILL language and VP of engineering at Ciranova, has expanded his role to include the chief operating officer position for the company.

“These two management moves by Ciranova come at an opportune time for us as we prepare to formally roll out our first complete solution for cell generation. Michael brings a wealth of EDA, IP and IC knowledge and experience that will be critical as we evolve our strategy and build an ecosystem of industry support for our Open Access-based offering,” said Jim Solomon, a member of the Ciranova Board of Directors. “Ed has established himself in the industry as a superb technologist who also understands the dynamics of the back-end design market beyond just the technology. His contributions to Ciranova have been extremely valuable, and we are confident in the increased role he’ll play in running the company.”

Ma has more than 15 years of experience in the EDA, IP and IC industries. Most recently, he spent three years as the vice president of foundry and IP relationships at Magma Design Automation, focusing on the supply chain solution for Magma’s design community. Prior to that, he was the senior director of business development at Artisan Components for six years, responsible for driving worldwide end-user adoption of Artisan’s physical library products. He holds a B.S. Electrical Engineering and a M.S. Computer Engineering from the University of Southern California as well as an M.B.A. from the University of San Francisco.

“Michael is a tremendous addition to the team,” said Petrus. “Michael has a unique combination of experience with IP and EDA tools. His knowledge of the entire IP supply chain will help Ciranova accelerate our efforts to gain adoption for our technologies.”

Petrus joined Ciranova in the summer of 2004 bringing with him 20 years of experience in building engineering teams and developing complex software systems. Prior to that he spent 10 years at Cadence Design Systems where he was a key architect and developer of SKILL and other components of Cadence's DFII technology. He started his career in EDA as a software engineer at Daisy Systems and has also held engineering management positions at Military Advantage and Nanomix.

About Ciranova

Ciranova™ is an Electronic Design Automation (EDA) startup company, focused on building an open, interoperable, language-based cell generator technology for custom analog and mixed-signal (AMS) integrated circuit design. Ciranova's technology, which is the only layout generator development environment built from the ground up on the industry standard OpenAccess database from SI2, will bring the integration, productivity and migration benefits of modern language-based design to custom and AMS designers. The company's first product, PyCell Studio™, is available as a free download from its web site, www.ciranova.com. The company is headquartered in Santa Clara, California.

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Note to editors: Digital photo available upon request.