



For Immediate Release

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CIRANOVA PYCELL TECHNOLOGY INCLUDED IN TSMC iPDK FOR ANALOG/MIXED-SIGNAL IC DESIGN

Next-Generation TSMC 65nm PDK Leverages Strategic Partnership;
Ciranova Helix Layout Automation Also Supports iPDK

SANTA CLARA, Calif., July 21, 2009 – Ciranova, Inc. today announced that the company's PyCell technology has been included into the TSMC 65nm Interoperable Process Design Kit (iPDK), as part of a strategic collaboration between the companies announced in March 2009. The partnership was established to develop a more powerful PDK, physical intellectual property (IP) and design infrastructure for analog and mixed-signal content in nanometer SoCs. Future deliverables involve more complex IP and advanced design and re-use methodologies.

"Analog and mixed-signal IP has become a major focus in SoC design," said Tom Quan, deputy director of Design Service Marketing at TSMC. "The technology foundation we are developing in collaboration with Ciranova will enable TSMC customers to integrate analog and mixed-signal content into today's SoCs, and move that content to future SoCs at more advanced process nodes, ahead of their competitors. The iPDK represents a key step in this effort."

Ciranova also announced that its Ciranova HelixTM device-level floorplanning and placement software supports the iPDK, and can be used "out of the box" in iPDK-based customer design flows.

"Older-technology and fragmented PDK strategies have slowed development and adoption of more powerful analog/mixed-signal design and IP integration flows," said Eric Filseth, CEO of Ciranova. "By making a single 'golden' iPDK available for any combination of custom design tools, TSMC is leading the industry toward higher level of innovation and automation. And by adding Helix, TSMC customers can immediately add automation to their existing layout environments."



Availability

Ciranova PyCell Studio and Helix are available now from Ciranova. For more information please visit <http://www.ciranova.com>. The iPDK is available from TSMC. For more information please visit <http://online.tsmc.com/online/>.

About Ciranova

Ciranova is an electronic design automation (EDA) company focused on very large productivity improvements in analog, RF and mixed-signal IC physical design. Complementary to existing design flows, Ciranova technology dramatically reduces the time and effort needed to develop device-level layout at both the circuit and PDK levels, while delivering results of equal or better quality than purely handcrafted methods. Ciranova supports the Si2 OpenAccess database.

For more information please visit <http://www.ciranova.com>.

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