For Immediate Release

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CIRANOVA TO HOLD PRIVATE DEMONSTRATIONS OF BREAKTHROUGH ANALOG/CUSTOM LAYOUT TECHNOLOGY AT DESIGN AUTOMATION CONFERENCE

New technology builds on PyCell™ leadership;
Enables creation and migration of analog and custom circuits across technology nodes

SANTA CLARA, Calif. – May 28, 2007 – Ciranova™, Inc, an EDA start-up developing open and robust layout generation technology for custom design, will conduct private, by-appointment-only demonstrations of a new software component technology for automating analog and custom layout at the Design Automation Conference, June 4-7 in San Diego, California. Building on the success of PyCell Studio™, Ciranova's free stand-alone environment for creating interoperable OpenAccess PCells, this new technology brings important new advances to current physical design flows, including:

- Fast turnaround time for rapid design exploration and tradeoff analysis. The ability to explore multiple device placements within minutes for a 20-50 device circuit, or a few hours for 500-1000 devices, enables enormous productivity gains. with handcrafted-like results.
- Hierarchically applied constraint groups that enable easy representation of multiple symmetries and complex structure. This is the intuitive automation control mechanism which has been lacking in previous attempts at physical automation.
- Preservation of design intent across multiple process nodes. Design intent is independent of device sizing and process data—it is the topological information, specific to each circuit, which experienced designers impart to their circuits

"Ciranova has significantly advanced the state of the art with this technology," said Jim Solomon, founder of Cadence Design Systems and member of the Ciranova Board of Directors. "Analog designers at several major semiconductor houses have been surprisingly receptive to these new

tools. Usually there is resistance in the analog community to new design flows. But when the need is great enough, that resistance goes away, and that is what we are seeing."

Solomon continued, "This new technology starts with PyCells, which are a very strong open foundation, and delivers automated, correct-by-construction physical layout. Ciranova is automating the labor-intensive parts of the analog physical design process, while integrating tightly within existing design environments. This is a revolutionary step forward for automated analog and custom design."

This new technology is based on the latest open standards and databases including the OpenAccess design database and PyCells, created by Ciranova's free product PyCell Studio. The technology is implemented as a software component that can be incorporated into existing tool flows utilizing OpenAccess. It is designed to integrate tightly with design, layout and analysis tools already in use.

"Ciranova is filling a major hole in the analog flow," said Jim Hogan, former Cadence Executive Fellow and member of the Ciranova Board of Directors. "A device placer is a highly valued and critical component in automated digital flows, but it's been missing from analog flows until now. Ciranova's technology is going to fill that gap, which will enable analog designs to be as reusable as digital designs."

Ciranova will provide previews of the technology at the Design Automation Conference in San Diego on June 4-7, 2007. Interested parties may schedule a private preview by contacting Ciranova at +1 408-553-6083 or via email at info@ciranova.com.

About Ciranova

Ciranova is an Electronic Design Automation (EDA) startup company, focused on building open, interoperable technology for analog and mixed signal (AMS) physical design automation. Ciranova's technology, built from the ground up on the industry standard OpenAccess database from Si2, will bring the integration, productivity and migration benefits of modern automation tools to custom and AMS designers. The company's first product, PyCell Studio™, is available as a free download from its web site, www.ciranova.com. The company is headquartered in Santa Clara, California.

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