



For Immediate Release

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NEW ONLINE DEMO SHOWS INTEROPERABLE PCELL LIBRARY SUPPORT BY FIVE EDA VENDORS

Five IPL Alliance members demonstrate that a single PCell library supports a complete custom IC flow including schematic, layout and DRC tools, enabling design database interoperability

SANTA CLARA, Calif. – February 21, 2008 – The Interoperable PCell Library (IPL) Alliance announced today that an interoperability demonstration featuring six products from five alliance members is now available for online viewing at <http://www.si2.org/interopdemo>. In the demonstration, each tool operates on the same OpenAccess database, with full PCell functionality, without resorting to any translation or stream-out operations.

The products shown in the demonstration include:

- PyCells™ from Ciranova™
- Laker™ from Silicon Canvas™
- RDE Framework™ from Silicon Navigator™
- Hercules™ and OAViewer from Synopsys®
- Calibre® from Mentor®

PyCells have also been tested for full interoperability with OpenAccess versions of Cadence® Virtuoso®.

The demonstration includes schematic capture; layout editing with PyCell manipulations including changing parameters, stretch handles, and automatic abutment; design rule checking and DRC error overlay on layout. Throughout the demonstration, connectivity is maintained as the tools open, edit and save the database.

The actual PyCell library used in the demonstration, including source code, is available for free download from the IPL Alliance web site at <http://www.iplnow.com>. PyCell Studio, the mechanism chosen by the IPL Alliance to create the proof-of-concept library, is available for free download from the Ciranova web site at <http://www.ciranova.com>.

“The IPL Alliance members have worked hard to ensure that their tools interoperate with full functionality,” said Eric Filseth, CEO of IPL founding member Ciranova, Inc. “This joint demonstration shows a level of interoperability which is unprecedented in the EDA industry.”

Interoperable PCell libraries have many advantages to semiconductor companies, foundries and EDA vendors. Semiconductor companies will be able to use common PCell libraries providing advanced functionality across multiple processes, reducing development and support costs while

increasing layout flexibility. Foundries will be able to reduce their PDK development costs while dramatically increasing the number of tools they support. EDA vendors will also be able to reduce PDK development costs, while supporting a wider range of foundry partners.

Inquiries regarding the online demonstration or the IPL Alliance may be directed to:

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