



For Immediate Release

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INTEL CAPITAL INVESTS IN CIRANOVA

SANTA CLARA, Calif., September 14, 2010. Ciranova Inc., a technology leader in analog IC physical design automation, announced today that Intel Capital, Intel Corporation's global investment organization, has made an investment in the company. The funding will be used to expand the company's sales and customer support operations and for specific extensions to the company's technology.

Ciranova's automated-design software helps engineers integrate complex RF, analog and mixed-signal content into nanometer-scale silicon much more densely and quickly than previously possible. Ciranova® technology is used by semiconductor companies integrating Wi-Fi, Bluetooth, SERDES and other functions into high-end system-on-chip (SoC) designs at 65nm and below.

"Silicon for consumer devices, especially mobile internet and broadband, now demands very aggressive mixed-signal integration in nanometer CMOS," said Eric Filseth, Ciranova CEO. "But putting many radios or other analog functions into a single chip, together with digital, is extremely challenging using traditional analog methods. Our approach is much better suited to the complex process rules and large-scale designs associated with SoCs, and we're delighted that Intel Capital has chosen to invest in our company."

"The productivity gap in analog/mixed-signal design automation has been growing at a much faster rate than the productivity gap in digital design. Increased automation in analog design flows is needed to ensure acceptable design times for analog/mixed-signal circuits" said Shishpal Rawat, director of business enabling programs with the Design Technology Solutions Group,

Intel. "Ciranova's approach could improve turnaround time in analog layout automation, a key component of the overall analog/mixed-signal design flow."

About Ciranova

Ciranova is an electronic design automation (EDA) company focused on large productivity improvements in RF, analog and mixed-signal IC physical design. Complementary to existing design flows and supported by leading foundries, Ciranova's PDK and layout automation software dramatically reduces the time and effort needed to implement complex analog functionality in advanced CMOS. Ciranova is a founding member of the Interoperable PDK Libraries (IPL) Alliance and supports the Si2 OpenAccess database. For more information, visit our website at www.ciranova.com.

About Intel Capital

Intel Capital, Intel's global investment organization, makes equity investments in innovative technology start-ups and companies worldwide. Intel Capital invests in a broad range of companies offering hardware, software, and services targeting enterprise, home, mobility, health, consumer Internet, semiconductor manufacturing and cleantech. Since 1991, Intel Capital has invested more than US\$9.5 billion in over 1,050 companies in 47 countries. In that timeframe, 175 portfolio companies have gone public on various exchanges around the world and 241 were acquired or participated in a merger. In 2009, Intel Capital invested US\$327 million in 107 investments with approximately 50 percent of funds invested outside the United States and Canada. For more information on Intel Capital and its differentiated advantages, visit www.intelcapital.com.

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