



**For Immediate Release**

For more information, contact:

Dave Millman  
Ciranova, Inc.  
+1 408 553-6083  
[dave@ciranova.com](mailto:dave@ciranova.com)

**CIRANOVA CLOSES RECORD SALES YEAR IN 2011**

January 5, 2012, Santa Clara California. Ciranova®, the automation technology leader for custom IC physical design, today announced it had achieved record business volume in 2011. The company's annual sales doubled over the 2010 year, driven in particular by demand at 28nm and below for Ciranova Helix®, the company's analog/mixed-signal floorplanning and placement software.

Contributing to the Helix business were orders from new customers, including two top-10 fabless IC companies, and reorders from multiple existing customers. The company also exited the year with its largest backlog to date.

"Placement, including layout-dependent effects, has become the most complicated and critical step in nanometer custom layout," said Eric Filseth, CEO of Ciranova. "Once you automate this, large improvements in design time and predictability become achievable, together with much closer synchronization of layout with circuit design. This has been a major draw. At 28nm and beyond, nobody wants to spend months in manual layout without knowing how long it will take for everything to turn out right."

2011 also saw adoption of Ciranova's PyCell™ technology for interoperable parameterized cells by several leading-edge semiconductor companies at the 20nm node.

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## **About Ciranova**

Ciranova is an electronic design automation (EDA) company focused on large productivity improvements in custom IC physical design. Complementary to existing design flows, Ciranova technology dramatically reduces the time and effort needed to develop transistor-level layout at both the circuit and PDK levels. Ciranova is a founding member of the Interoperable PDK Libraries (IPL) Alliance and supports the Si2 OpenAccess database. For more information please visit <http://www.ciranova.com>.

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