



## **For Immediate Release**

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### **CIRANOVA CLOSES RECORD SALES YEAR IN 2009**

Santa Clara, Calif., February 3, 2010. Ciranova, the automation technology leader for custom IC physical design, today announced it had achieved a record sales year in 2009. The company's bookings tripled over the 2008 year, driven by strong demand in major accounts for the company's Helix™ analog/mixed-signal floorplanning and placement software, for use at advanced technology nodes.

2009 also saw record bookings for Ciranova's PDK support and consulting practice. The company's Python-based interoperable parameterized cell ("PyCell") technology was adopted by several top-10 semiconductor companies, including the TSMC iPDK, announced in July 2009.

"Moore's Law is alive and well," said Eric Filseth, CEO of Ciranova. "Our rapid growth in an incredibly challenging economy is a sign of the resilience of the semiconductor industry, which continued to invest in sub-65nm silicon, new system-on-chip products, and new EDA technology despite the downturn."

#### **About Ciranova**

Ciranova is electronic design automation (EDA) company focused on large productivity improvements in RF, analog and mixed-signal IC physical design. Complementary to existing design flows, Ciranova technology dramatically reduces the time and effort needed to develop device-level layout at both the circuit and PDK levels. Ciranova is a founding member of the Interoperable PDK Libraries (IPL) Alliance and supports the Si2 OpenAccess database.

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