



For Immediate Release

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SILICON CLOCKS TAPES OUT TWO CHIPS USING CIRANOVA PYCELLS
Pioneering MEMS PyCell Library Fully Implemented Six Times Faster Using PyCell Studio

SANTA CLARA, Calif. – July 20, 2009 – Ciranova, Inc. announced today that Silicon Clocks had successfully taped out two MEMS chips, implementing the company's modular CMEMS™ (Standard CMOS + MEMS) process technology platform, using a process design kit (PDK) created in part using PyCell Studio.

“With CMEMS™, we are bringing a monolithically integrated MEMS platform technology to market,” said Emmanuel Quevy, CTO of Silicon Clocks. “It was crucial for us to choose the most productive and interoperable building blocks for our flow. We chose PyCell Studio because PyCells are fully compatible with our in-house Virtuoso flow from Cadence Design Systems, and also interoperable with other tools our customers may use. The productivity and advanced geometric capability of Ciranova's PyCell technology has dramatically lowered the cost and complexity of our efforts to create a standardized CMOS-MEMS design kit that integrates seamlessly with any CMOS PDK or design flow.”

Historically, MEMS technologies have required boutique processing and complicated packaging strategies. Silicon Clocks' innovative CMEMS technology enables developers to simultaneously integrate many mechanical devices directly on top of CMOS wafers using more standardized semiconductor fabrication techniques. PyCells are an integral part of the PDK Silicon Clocks has created to support CMEMS.

“It took me close to two years to create our original MEMS cell code base. It took me under four months to replicate that using PyCells, including my learning curve, which was really short,” said

David Bernstein, Device Engineer at Silicon Clocks. “The advanced PyCell API functions save a lot of time compared to other approaches.”

PyCell Studio is a complete development environment for creating interoperable OpenAccess PyCells. Available as a free download from <http://www.ciranova.com>, PyCell Studio has been downloaded by over 1000 users at 250 companies worldwide.

About PyCells

Ciranova interoperable PyCells are a next-generation approach to parameterized cell development for custom IC design. Unlike older PCell approaches, the PyCell architecture is design-rule aware and generates DRC-correct device geometry automatically; the result is a major reduction in user coding effort, particularly at nanometer process nodes. PyCells run in any OpenAccess-capable EDA tool, and were selected as the physical foundation for the Interoperable PDK Alliance at <http://www.iplnow.com>. The PyCell Studio development system is a free download from <http://www.ciranova.com>.

About Silicon Clocks

Silicon Clocks, a fabless semiconductor company headquartered in Silicon Valley, develops best-in-class integrated timing products based on its modular CMEMS™ (Standard CMOS + MEMS) process technology platform. Serving a range of electronic systems including high speed serial data, wireless communications and consumer products, Silicon Clocks delivers improvements in performance, reliability and flexibility combined with reductions in power, size, and part count to provide significant system cost savings. For more information, visit <http://www.siliconclocks.com>.

About Ciranova

Ciranova is an electronic design automation (EDA) company focused on very large productivity improvements in analog, RF and mixed-signal IC physical design. Complementary to existing design flows, Ciranova technology dramatically reduces the time and effort needed to develop device-level layout at both the circuit and PDK levels, while delivering results of equal or better quality than purely handcrafted methods. Ciranova supports the Si2 OpenAccess database. For more information please visit <http://www.ciranova.com>.

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